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501.42645VX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: S. SHUKURI
Application No.: 10/767,069
Filed: January 30, 2004
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND A
METHOD OF MANUFACTURING THE SAME
Art Unit: Unknown
Examiner: Unknown

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§ 1.97 & 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 26, 2004

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy of a communication from a foreign patent office in a counterpart foreign application and copies of the documents listed in the attached form PTO/SB/08A for the Examiner's consideration.

This Information Disclosure Statement is being submitted, on information and belief, before the mailing date of a first office action on the merits.

Each of the documents listed on the attached form PTO/SB/08A is in the English language.

It is respectfully requested that this Information Disclosure Statement be considered by the Examiner.

Application No.: 10/767,069

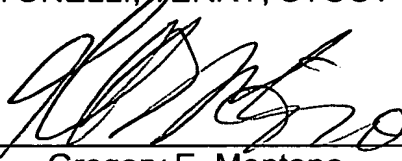
Docket No.: 501.42645VX1

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Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

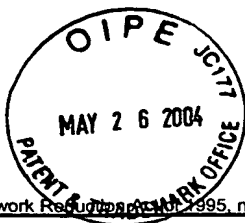
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PTO/SB/08A (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449A/PTO		Complete if Known	
		Application Number	10/767,069
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing Date	January 30, 2004
		First Named Inventor	S. SHUKURI
		Art Unit	Unknown
		Examiner Name	Unknown
(use as many sheets as necessary)		Attorney Docket Number	501.42645VX1
Sheet	1	of	1

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
		6,335,554		01-01-2002	Yoshikawa	
		6,632,714		10-14-2003	Yoshikawa	

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)					

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
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		Takaaki NOZAKI, et al., A 1 Mbit EEPROM with MONOS Memory Cell for Semiconductor Disk Application, 1990 Symposium on VLSI Circuits, 1990 IEEE, (pp 101-102)	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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